

FIG. 1A (PRIOR ART)

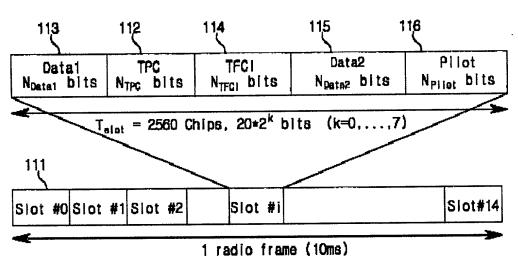


FIG. 1B (PRIOR ART)

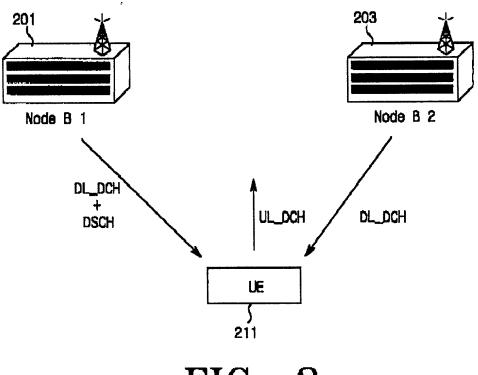


FIG. 2 (PRIOR ART)

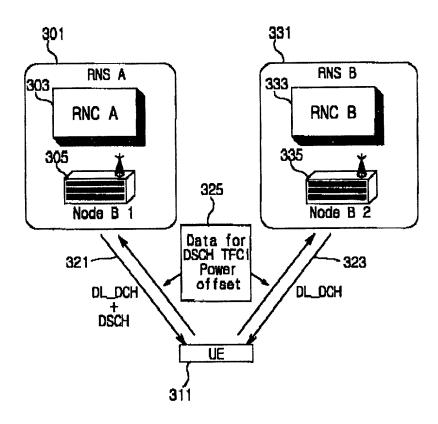


FIG. 3 (PRIOR ART)

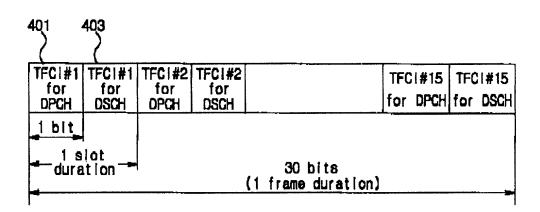


FIG. 4

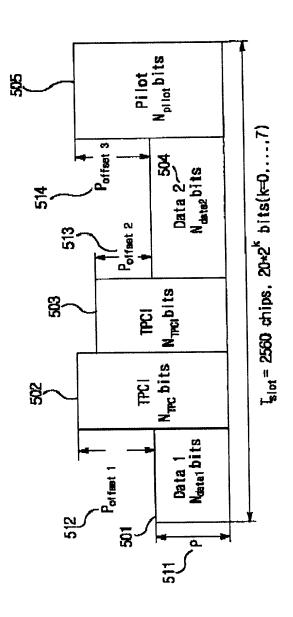


FIG. 5

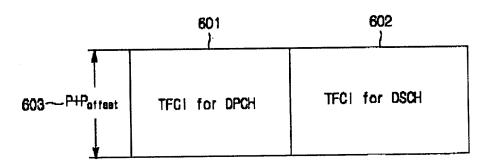


FIG. 6A

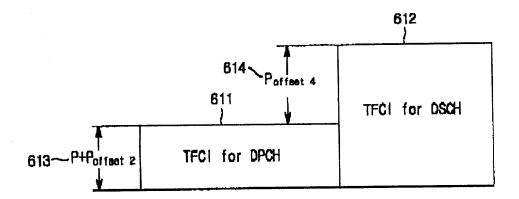


FIG. 6B

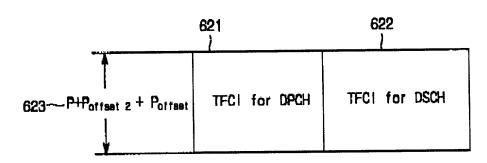


FIG. 6C

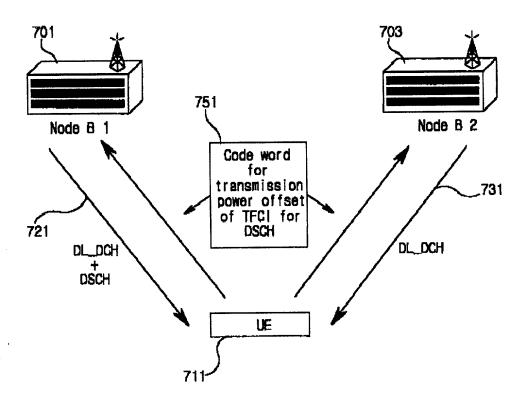


FIG. 7

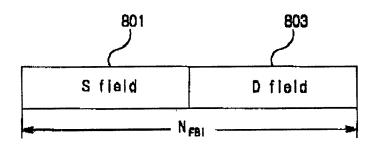


FIG. 8A

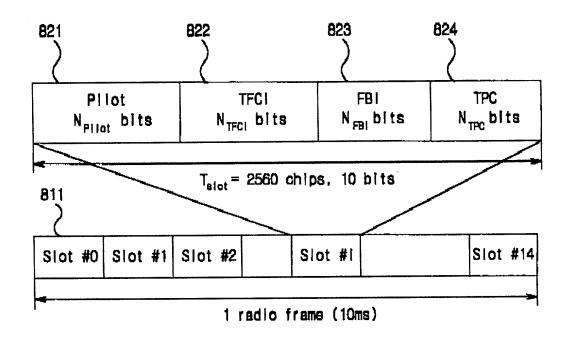
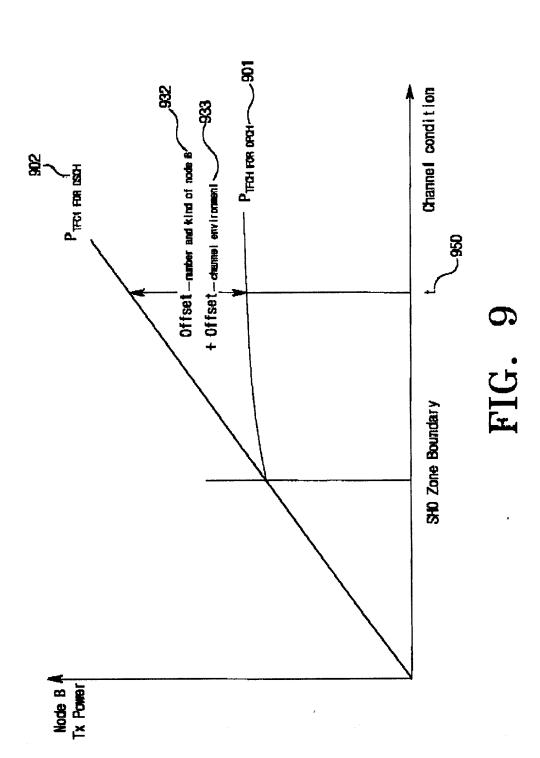


FIG. 8B



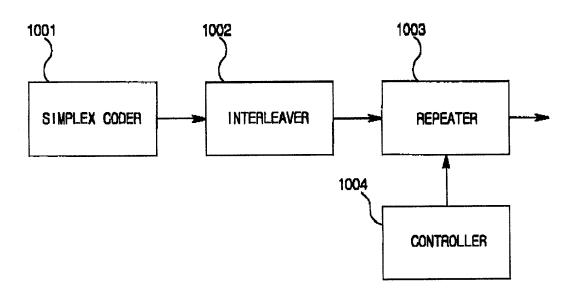


FIG. 10

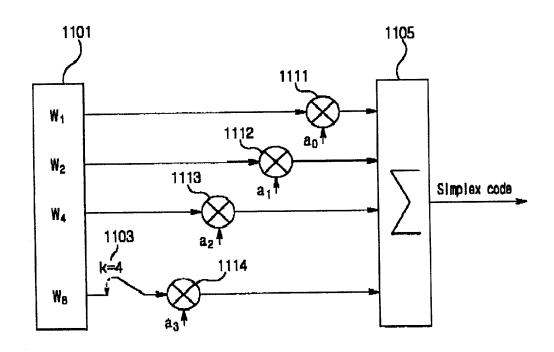


FIG. 11

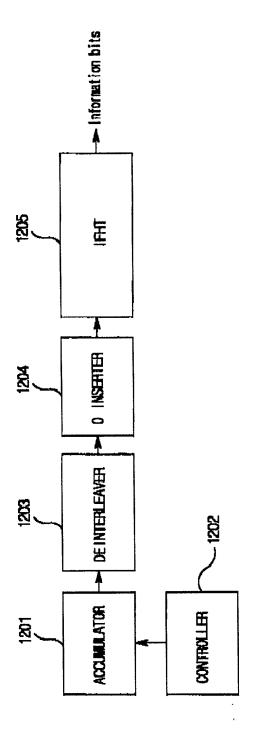
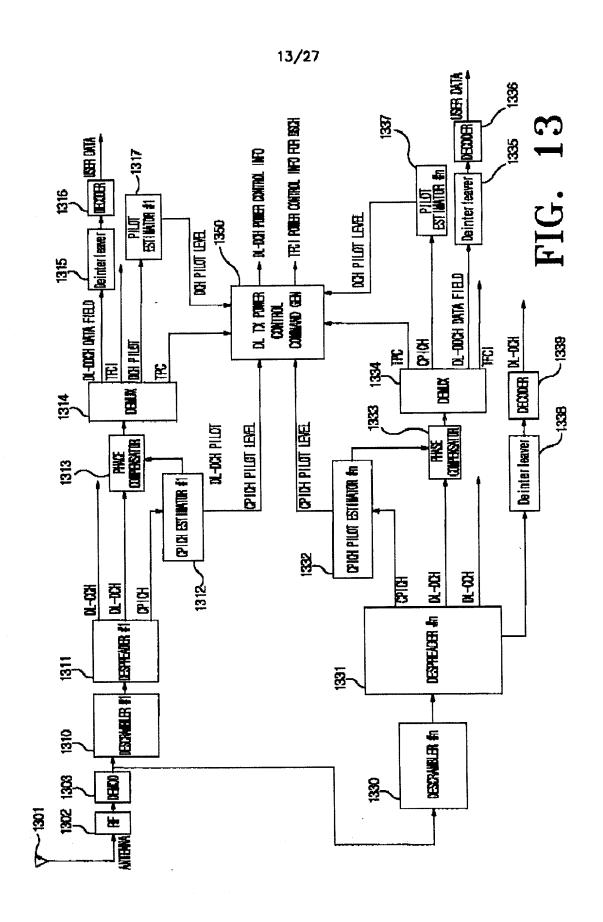
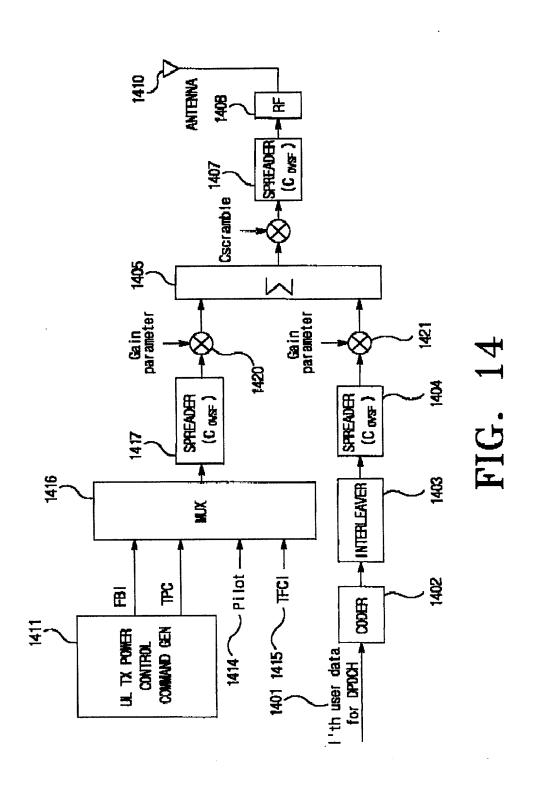


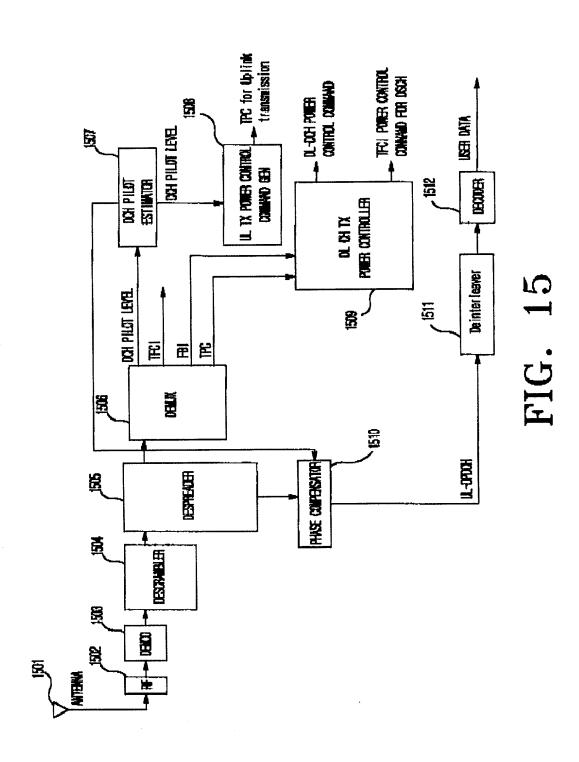
FIG. 12

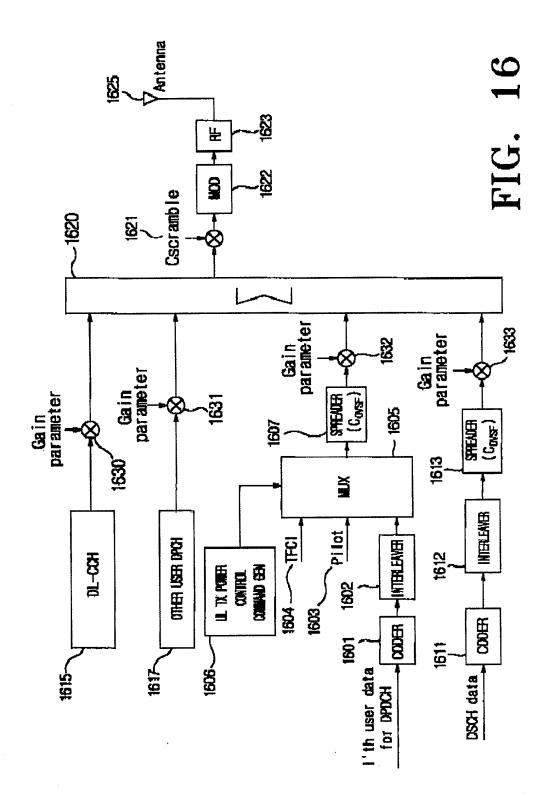
..) }



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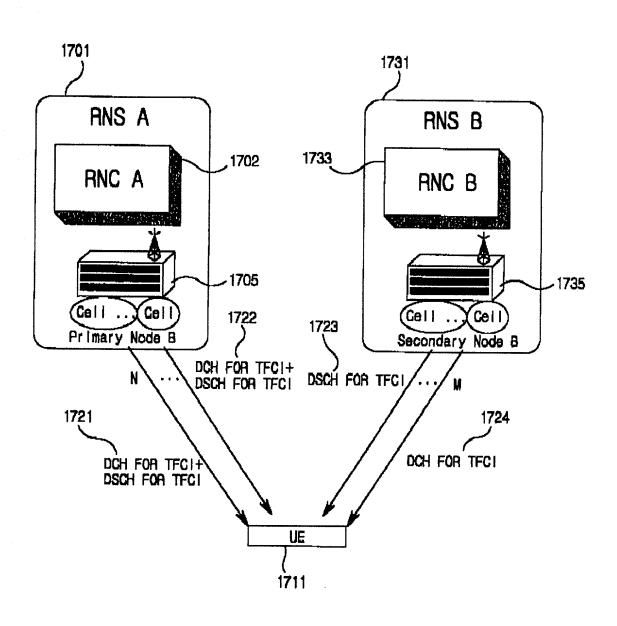
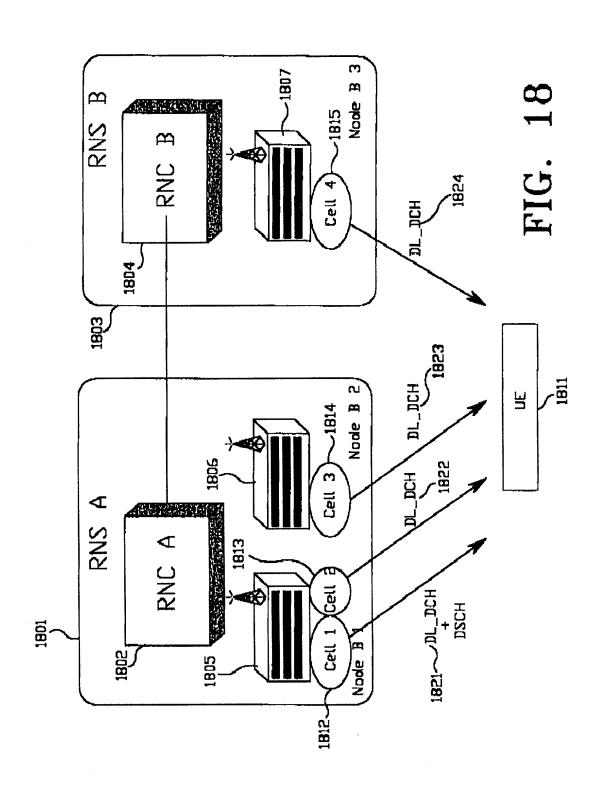


FIG. 17



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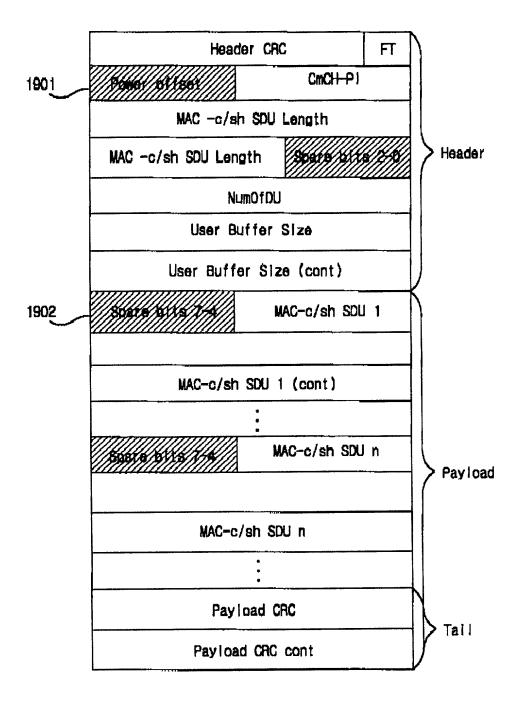


FIG. 19

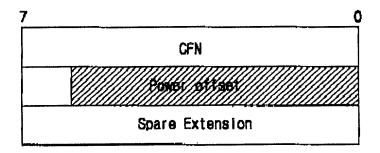


FIG. 20

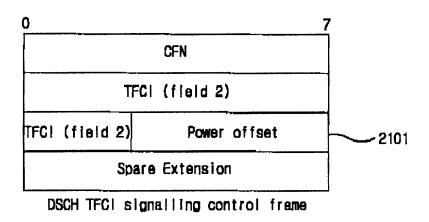


FIG. 21

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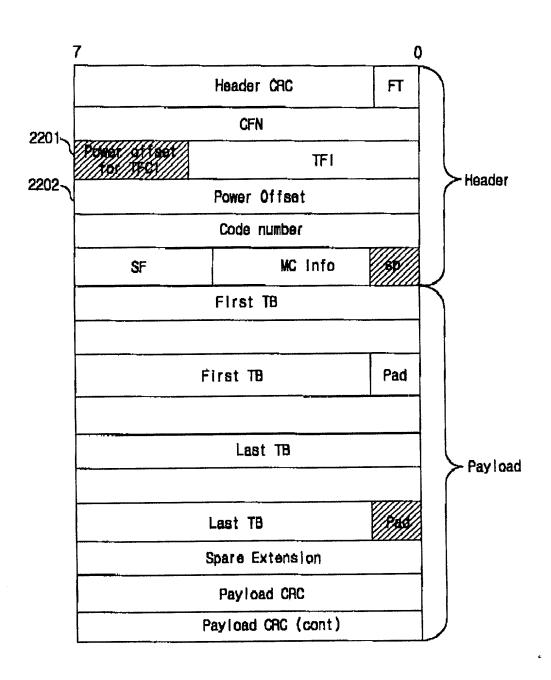


FIG. 22

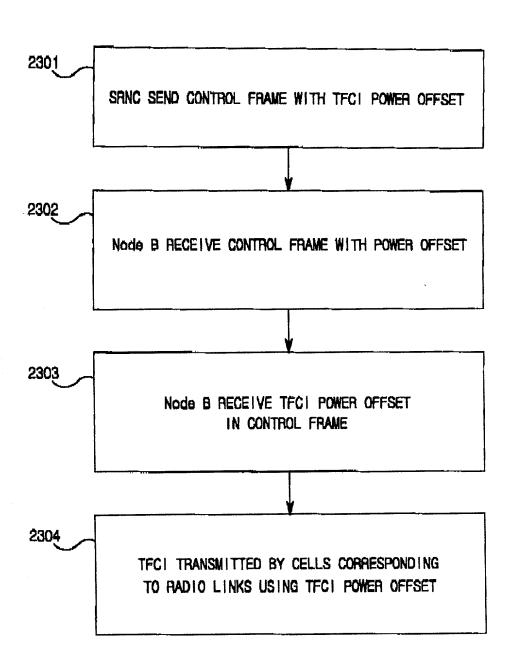


FIG. 23

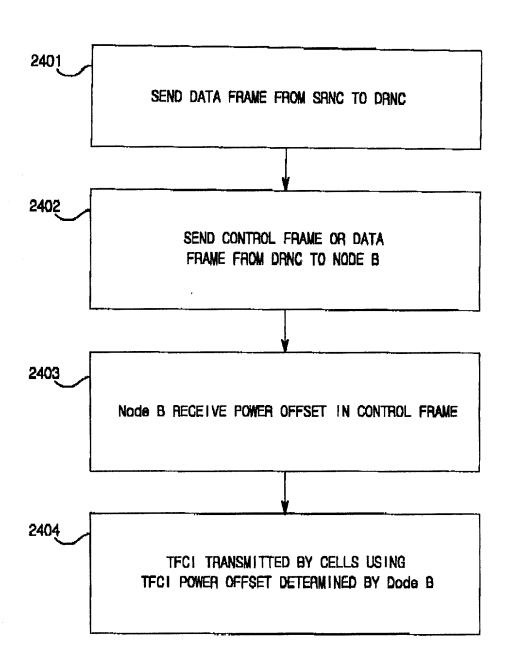


FIG. 24

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9. 1.36 RADIO LINK SETUP REQUEST 9. 1.36. 1 FDD message

E/Croup None	T Branches	04	15 45-0 0-0	Campallan	Celtlanlitu	An lend
E/Group Name	Presence	Range	1E type and reference	Semantica description	Criticality	rasigned Criticality
Message Discriminator	M		9,2,1,45	need thrinti	 	CITTICETITY
Message Type	TM	 	9.2.1.46		YES	to out
CANC Communication	M	 	9.2.1.18		YES	reject reject
context ()	M		8.2.1.10	Į.	162	ialect
Transaction ID	М	-	9.2.1.62			
UL DPCH Information	in .	1	0.4.1.02	 	YES	16 6ct
XL Scrambing code	M		9.2.2.59		133	.0,000
Min UL Channellastion	<u> </u>		9.2.2.22			
Code Length	197		U.E.E.E.E		. —	
>Nax Number of UL. DPDCHs	C -		9,2.2.21			
	CodeLen					
>puncture limit	M		9.2.1.50	For UL		
>TFCS	M M		9.2, 1.58	For UL		
XIL DPCCH Solt Format	M		9.2.2.57			
XIL SIR Target	N		UL SIA			
	ļ.,		9.2.2.58			
>Diversity mode	M		9.2.2.9			
>SSOT cell ID Length	0		9.2.2.45			
>S Fleid Length	C-FBI		9.2.2,40			
OL DPCH Information					YES	re ect
>TFCS	M		9.2.1.58			
>OL OPCH Slot Format	M		9.2.2.10			
>TFCI signalling mode	M		9.2.2.10 9.2.2.50			
>TFG1 presence	C - SlotFormat		9.2.1.57			
Multiplexing Position	M		9.2.2.29			
>POSCH PLID	C-DSCH		AL ID			
			9.2.1.53			
>POSCH code Mapping	C-DSCH		9.2,2.25			
>Power Offset Information		1				
>>P01	M		Power Offset 9.2.2.29	Power offset for the TFCI bits		
>>P02	M		Power	Power offset for the TPC		
>×P03	M		Power	Power offset for the pilot bits		
米DO TPC DL Step Size	М		9.2.2,16			
The Rest Omitted						

FIG. 25

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9. 1.36 RADIO LINK SETUP REQUEST 9. 1.36. 1 FDD message

IE/Group Name	Presence	Range	1	Semantics	Criticality	
Name - Name -			reference	description		Criticality
Message Discriminator	M	ļ	9.2.1.45			
Message Type			9.2.1.46		YES	reject
CPNC Communication context ID	M		9.2.1.18		YES	reject
Transaction ID	M		9.2.1.62			
UL DPCH Intermet on					YES	reject
>UL Scrambing code	M		9.2.2,59		7	
>Min UL Channel (sation Code Length	M		9.2.2.22			
>Max Number of UL	G -		9.2.2.21			
<u>DPDCHa</u>	CodeLen					
>puncture limit	M		9.2.1.50	For UL		
>TFC\$	M		9.2.1.58	For UL		
XUL OPCCH Solt Format			9.2.2.57			
XL SIR Target	M		UL SIR			
			9.2.2.58 9.2.2.9			
>Diversity mode	M		9.2.2.9			
>SSOT call ID Length	0		9.2.2.45			
>S Field Length	C-FBI		9.2.2.40			
DL DPCH Information					YES	re]ect
>TFCS	М		9.2.1.58	For DL		
>OL OPCH Slot Format	M		9.2.2.10			
>TFC: algnailing mode	M		9.2.2.50		=	
>TFCI presence	C -		9.2.1.57			
	SlotFormat					
Multiplexing Position	М		9.2.2,29			
>POSCH RLIO	C-DSCH		AL ID			
			9.2.1.53			
>POSCH code mapping	G-DSCH		9,2,2,25		_	
>Power Offset Information		1				
>>P01	M		Power	Power offset		
	100		Offset	for the TFCI		
			9.2.2.29			
>>P02	M		Power	Power offset		
			Offset	for the TPC		
			9.2.2.29	bits		
>>P03	M		Power	Power offset		
			Offset	for the pilot		
			9.2.2.29	bits		
>>P04	M	-		Power offset		
	1			for the TFCI		
			9.2.2.29	bits for DSCH		

The Rest Omitted

FIG. 26

9. 1.47 RADIO LINK RECONFIGURATION FRQUEST

9. 1.47. 1 FDD message

(E/Group Name	Presence	Range	IE type and Reference	Semantics Description	Criticality	Assigned Criticality
Message Discriminator	M		9.2.1.45			
Message Type	M		9.2.1.46		YES	reject
Node B Communication Context ID	M		9.2.1.48	The reserved value "ALL NBCC" sjall not be used.	YES	reject
Transaction ID	M		9.2.1.62			
UL DPCH information		01			YES	reject
>TFCS	0		8.2,1.58	For the UL.		
DL DPCH information		01			YES	reject
>TFCS	0		9.2.1.58	For the UL.		
>TFC Signalling Mode	0		9.2.2.50			
>Limited Power increase	0					
≻Power Offset Information		1				
>>P01	M		Power Offset 9.2.2.29	Power offset for the TFC! bits		
>>P04	М		Power Offset 9.2.2.29	Power offset for the TFGI bits for OSCH		

The Rest Omitted

FIG. 27

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